



EV697604697

PTO/SB/08B (08-03)

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of

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Application Number

10/706,493

Filing Date

11/12/2003

First Named Inventor

Jeffrey A. Andrews

Art Unit

2676

Examiner Name

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Attorney Docket Number

MS1 1373US

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
HN		Yamachi, Hidemi et al., "A Technique for Object and Collision Detection by Z-buffer," Information Processing Society, Vol. 43, No. 6, June 2002, pp 1899-1909.	—
HN		White, Martin et al., "The Tayra 3-D Graphics Raster Processor," Comput. & Graphics, Vol. 21, No. 2, pp. 129-142, 1997.	—
HN		Chen, Cheng-Hsien et al., "Reduce the Memory Bandwidth of 3D Graphics Hardware with a Novel Rasterizer," Journal of Circuits, Systems, and Computers, Vol. 11, No. 4 (2002) pp. 377-391.	—
HN		Bresenham, Jack, "Teaching the graphics processing pipeline: cosmetic and geometric attribute implications," Computers & Graphics 25 (2001), pp 343-349.	—
HN		Doctor, Louis et al., "Using raster scan in color graphics," Raster Technologies, Mini-Micro Systems, December 1981, pp. 102-107.	—
HN		Whitted, T., "Hardware Enhanced 3-D Raster Display System," CMCCS '81 - ACCHO '81, pp 349-356.	—
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HN		Parke, Frederic I., "Simulation and Expected Performance Analysis of Multiple Processor Z-Buffer Systems," Computer Engineering, Case Institute of Technology, pp 48-56.	—

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Date Considered

10/14/2005

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